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JUL 2 8 2003 &						
Application Serial Number: 09/850,254		Filing Date: 05/07/2001		Examiner: Quinto, K.		Group Art Unit
Invention: Ser Ma	niconductor App nufacturing Metl	aratus In nod Ther	cluding a Multi- efor	-Layer Wiring	Configuratio	n and
TO THE COMI Transmitted her shown below.	MISSIONER OF I	PATENTS Iment in the	S AND TRADEN he above-identific	MARKS: ed application.	The fee has be	een calculated as
		C	LAIMS AS AMI	ENDED		
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	7	MINUS	20	0	\$18	\$ 0.00
NDEP. CLAIMS	2	MINUS	3	0	\$84	\$ 0.00
а	mall entity status overified statement verified statement	TAL AD	DITIONAL FEE plication under 37	CFR 1.9 and 1	MENDMENT 1.27 has been e	
^\	harge \$to	Deposit A	is attached.			
tin	the event an exte	nsion of 1 R. §1.136	time is required	please conside	er this a notitio	ly filed. However, on for extension of equired to Deposit
<u> つ/</u> da	νς/υ <u>}</u> te	Do	Darryl Walker Reg. No. 43,232	2		RECEIVED JUL 31 2003
mail in an er	tify that this correspondence is being depondence addressed to the con \(\frac{1}{2}\)/(2)	sited with t	he United States Pos	tal Service as first	ologo	VED 2003 ENTER 2800

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